

ABSTRACT OF THE DISCLOSURE

SUB A2

In a liquid crystal display comprising a second substrate 20 for carrying a liquid crystal 40 and data lines 22 formed on the surface of the second substrate, a third insulating layer 246 for covering the data lines 22 are formed on the surface of the second substrate 20. On the surface of the third insulating layer 246, chip connecting wiring 25 are formed over a covered region 61 covered with a sealing material 30 and the liquid crystal 40 and the region other than the covered region 61. By connecting the chip connecting wiring 25 to the data lines 22 via contact holes 25a formed within the covered region 61 of the third insulating layer 246, corrosion of the wiring formed on the substrate is inhibited.

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A2
In a liquid crystal display that includes a second substrate that supports a liquid crystal and data lines formed on the surface of the second substrate, a third insulating layer that covers the data lines is formed on the surface of the second substrate. On the surface of the third insulating layer, chip connecting wiring is formed over a covered region covered with a sealing material and the liquid crystal and the region other than the covered region. By connecting the chip connecting wiring to the data lines via contact holes formed within the covered region of the third insulating layer, corrosion of the wiring formed on the substrate is inhibited.